EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	425	(chip or (micro near3 chip)) same (microprocessor or processor) same (memory or NVPAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWIBNT; IBM_TDB	AND	ON	2008/04/29
82	64	(chip or (micro near3 chip)) same (microprocessor or processor) same (memory or NNYAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authentication) near3 data) and ((author/zing or authenticating or denying) same (accessor readin or writing))	US-PGFUB; USPAT; USOOR; FPFS; EPO; JPO; DEFWENT; IBM_TDB	AND	ON	2008/04/29 10:58
\$3	67	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVFAM or (non near2 volatile near3 programmable near3 programmable near3 secure or authentication) or security or secure or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 10:58

S4	67	(chip or (micro near3 chip) or (smart near3 chip)) same (microprocessor or processor) same (memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (accessor reading or writing))	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 11:13
\$5	99	(chip or (micro near3 chip) or (smart near3 card)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USCOR; FPRS; EPC; JPO; DETWENT; IBM_TDB	AND	ON	2008/04/29 11:14
S6	18	(chip or (micro near3 chip) or (smart near3 card)) same (embed\$3) same (device or appliance or (circuit near3 board)) same (microprocessor or processor) same ((memory or NVFAM or (non near2 volatile near3 memory))) same ((protection or security or secure or authentication) near3 data) and ((authorizing or authenticating) or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USCOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29

S7	789	(chip or (micro near3 chip) or (smart near3 card)) same ((protection or security or secure or authorization or authentication) near3 data) and (authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DEFWENT; IBM_TDB	AND	ON	2008/04/29 11:25
S8	3121	713/193	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15
S9	576	711/101	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15
S10	3787	711/103	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15
S11	1496	711/104	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15
S12	28	St and S8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16
S13	1	St and S9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16
S14	11	St and St0	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16

S15	1	S1 and S11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16
S16	53	S7 and S8	US PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56
S17	2	S7 and S9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56
S18	3	S7 and S10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56
S19	0	S7 and S11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56
S20	2	"5933628".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 14:25
S21	2	"5933620".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 14:25
S22	2	"5293610".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM TDB	AND	ON	2008/04/29 14:26

S24	32	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVFAM or (non near2 volatile near3 programmable near3 memory))) same (protection or security or secure) same ((authorization or authentication) near3 data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/30 07:41
S25	108	(chip or (micro near3 chip) or (smart near3 card)) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing)) and (software and hardware) same ((protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25
S26	2	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) and (password or (protection near3 data)) same (allow or deny) same (access) and (control same (bit\$2 o signal\$2)) same (software or hardware) same (protection)	US PGPUB; USPAT; USOCR; FPRS; EPC; JPC; DERWENT; IBM_TDB	AND	ON	2008/08/25

S27	40	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVPAM or (non near2 volatile near3 programmable near3 memory)) and (password or (protection near3 data)) same (allow or deny) same (access) and (control same (bit\$2 or signal\$2)) same (software or hardware) same (protection)	US PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:04
S28	49	(password or (protection near3 data)) same (allow or deny) same (access) and (control same (bit\$2 or signal\$2)) same (software and hardware) same (protection)	US POPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:08
S29	1699	711/145	US PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:20
S31	30	S29 and "s7"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:21
S32	828	(chip or (micro near3 chip) or (smart near3 card)) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:21
S33	3	S29 and S32	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:21

S34	***************************************	(chip or (micro near3 chip)) same (microprocessor or processor) same (memory or NVFAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data)	US-PAPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25
S 35	2	S29 and S34	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:23
S36	10	("20020101763" "20030126513" "5794033" "5930826" "6031757").PN. OR ("6731536").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2008/08/25 14:01
S37	1392	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal \$2) same (hardware and software))	US-PGPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 14:58
S38	3372	713/193	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 14:59
S3 9	3307	711/163	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 14:59
S40	1149	711/164	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 14:59

S41	1699	711/145	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 14:59
S43	39	S37 and S38	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 15:00
S44	42	S37 and S39	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 15:00
S45	21	S37 and S40	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 15:00
S46	5	S37 and S41	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 15:00
S47	94	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or ket) same (password or ket) adata) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal \$2) same (hardware and software) same protection)	US PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 15:18
S49	685	(hardware same chip same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 15:57

S50	192	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authorix\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal \$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 16:00
S51	108	(chip or (micro near3 chip)) same (memory or rejector) same (password or key or (postection near3 data)) and (allow\$3 or deny\$3 or authorix\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal \$3 same (protection)) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 16:03
S52	29	(chip or (micro near3 chip)) same (memory or rejector) same (password or (postection near3 data)) and (allow\$3 or deny\$3 or authorix\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal \$3 same (neardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 16:04
S53	120	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PCPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 16:34

S54	28	(chip or (micro near3 chip) or (smart near3 card)) same (microprocessor) same ((memory or NVFAM or (non near2 volatile near3 memory))) same (password or key of (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/26 08:04
S55	1089951	(chip or (micro near3 chip) or (smart near3 card)) or (smart near3 card)) or (microprocessor) same ((memory or NVFAM or (non near2 volatile near3 memory))) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US PGPUB; USPAT; USOOR; FPRS, EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/26 08:08
S56	1058705	(chip or (micro near3 chip)) or (microprocessor or processor) same (memory or NNFAM or (non near2 volatile near3 programmable near3 memory))) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authorix\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (thardware) same protection)	US FOPUB; USPAT; USOOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ÓN	2008/08/26 08:10

S57	1058670	((memory or NVRAM or (non near2 volatile	US-PGPUB; USPAT; USCOR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/26 08:10
S 59	12	("5628023" "6260131" "6304944" "6351797" "6408373" "6412043" "6418523" "6430664" "6430670"). PN. OR ("6643759"). URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2008/08/26 08:12

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